

EAST - [506502d:1] EX

File View Edit Tools Window Help

BRS: 64

Pending

Active

- ✓ L2: (27336) user near9 register
- ✓ L3: (112806) specification near9 descript\$4
- ✓ L4: (4897) specification near9 instruct\$4
- ✓ L5: (157560) hardware and software and implement\$5
- ✓ L7: (14280) process\$3 and (file near5 register)
- ✓ L8: (13) 12 and 13 and 14 and 15 and 17
- ✓ L9: (6) wang-albert-ren-rui.inv.
- ✓ L10: (5) 18 and 19

Failed

Search: Search Clear

DBs: US PGPUB USPAT B1 Phrase

Default operator: OR Highlight terms initially

12 and 13 and 14 and 15 and 17

BR5.htm Edit Print New Help

#	Document ID	Issue Date	Title	Current OR	Current
1	<input checked="" type="checkbox"/> US 20040250231	20041209	Automated processor generation system for de	716/18	
2	<input type="checkbox"/>	20040073899	Instruction processor systems and methods	717/158	717/10
3	<input checked="" type="checkbox"/> US 20030208723	20031106	Automated processor generation system for de	716/1	
4	<input checked="" type="checkbox"/>	20020133784	Automatic design of VLIW processors	716/1	
5	<input type="checkbox"/>	20020120914	Automatic design of VLIW processors	716/17	
6	<input checked="" type="checkbox"/> US 6760888 B2	20040706	Automated processor generation system for de	716/1	712/1
7	<input checked="" type="checkbox"/>	US 6651222 B2	Automatic design of VLIW processors	716/1	716/2
8	<input checked="" type="checkbox"/>	US 6581187 B2	Automatic design of VLIW processors	716/1	
9	<input checked="" type="checkbox"/> US 6477697 B1	20021105	ADDING COMPLEX INSTRUCTION EXTENSIONS DEFINE	716/18	712/15
10	<input type="checkbox"/>	US 6477683 B1	Automated processor generation system for de	716/1	716/18
11	<input checked="" type="checkbox"/> US 6408428 B1	20020618	Automated design of processor systems using	716/17	716/18
12	<input type="checkbox"/>	US 6385757 B1	Auto design of VLIW processors	716/1	
13	<input checked="" type="checkbox"/> US 6167461 A	20001226	Programmable high performance disk formatter	710/5	360/7

✓ Text Details HTML

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